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Technical Co-Sponsored by
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Official Website
<http://www.a-sscc.org>

Important Dates:

Deadline of Early Registration:
Oct. 19, 2007 23:59 (GMT+0800)

Regular Registration:
After Oct. 19, 2007 23:59(GMT+0800)

The IEEE SSCS (Solid-State Circuits Society) fully supported Asian Solid-State Circuits Conference (A-SSCC) 2007 is an international forum for advances in solid-state circuits design held in Asia. Other than the conventional conference programs such as paper and plenary sessions, A-SSCC features a practical industry program. In the very third year under its name, the A-SSCC'07 received more than 345 papers. Technical program committee members are a balance of the industry and the academia and as a result, the technical program should interest both industry and academia.

Technical Paper Focus

"A 2Gb/s Bi-Directional Inter-Chip Data Transceiver with Differential Inductors for High Density Inductive Channel Array"

"0.67uW/MHz, 5ps Jitter, 4 Locking Cycles, 65nm ADDLL"

"A 130 nm CMOS 6-bit Full Nyquist 3GS/s DAC"

"A 512Mb 2-Channel Mobile DRAM with Shared Memory Array"

"A 470uW Multi-Modulus Injection-Locked Frequency Divider with Division Ratio of 2, 3, 4, 5 and 6 in 0.13um CMOS"

"A 10Gb/s Burst-mode Transimpedance Amplifier in 0.13um CMOS"

"A Low Power and High Picture Quality H.264/MPEG-4 Video Codec IP for HD Mobile Applications"

Plenary Talks

- **Dr. Heegook Lee** (President & CTO of LG Electronics Inc., Korea)
"The Strategic Considerations for Digital-TV System-on-Chip Products"
- **Mr. Satoru Ito** (Chairman of Renesas Tech., Japan)
"Convergence and Divergence in Parallel for the Ubiquitous Era"
- **Dr. Jackson Hu** (Chairman of UMC, Taiwan)
"Recent Business Models and Technology Trends and Their Impact on the Semiconductor Industry"
- **Dr. Willy Sansen** (Professor & Head ESAT-MICAS, Katholieke Univ. Leuven, Belgium)
"Analog Design Challenges in Nanometer CMOS Technologies"

Evening Panels

"SoC or SiP: What is the Best Solution in IC Business for Ubiquitous Mobile Platforms?"

Moderator: Joungho Kim (KAIST, Korea)

Panelists : C. Kim (Samsung), M. Aoyagi (AIST), T. Akazawa (Renesas Tech.), C. H. Lee (Amkor), C. P. Hung (ASE), A. Lu (SIMTECH), S. Y. Oh (ETRI), M. Nomura (NEC), T. L. Wu (NTU)

"Display Driver IC's: Next Technology and Market Trends"

Moderator: Oh-Kyong Kwon (Hanyang Univ., Korea)

Panelists: M.-H. Lee (Samsung), S. Saito (NEC), H.-K. Chung (Samsung SDI), C. T. Liu (AUO)

Industrial Program

This program is inspired by Nicky Lu, a fellow of the IEEE. In the industry program, papers on cutting-edge IC products are presented with application, designed measurement information. There are two parallel sessions with 8 papers from IT-SoC Association (Korea), Intel (USA), Hitachi (Japan)/Renesas Tech. (Japan), Sun Microsystems (USA), Phoenix Microelectronics (China), Hynix (Korea), VIA Technology(Taiwan), and Renesas Tech.(Japan).

Student Design Contest

7 outstanding chip designs from the accepted student papers will make real demonstrations on the conference site with authors' explanation. The best student design will be selected among those 7 demonstrations reflecting the high research standards of Asian academies.

Tutorials

- **Analog** (David Robertson, Analog Devices)
"An introduction to analog-to-digital converter design – old problems and new challenges"
- **Wireless** (Huei Wang, NTU)
"Silicon mm-wave IC design"
- **Digital/Memory** (Stefan Rusu, Intel)
"Advanced leakage reduction techniques"
- **Emerging Technology** (Hoi-Jun Yoo, KAIST)
"Low energy CMOS systems for body sensor network and ubiquitous healthcare"