

11/4

8:00~8:30 Navis C

Nov. 4	Navis-C PC1	Navis-B PC2	Navis-A PC3	Argos-F PC4	Argos A&B	Nire	Kusu
08:30-08:40 (10)	Opening Ceremony						
08:40-08:50 (10)	Welcome Speech of Fukuoka Prefectural Governor						
08:50-09:35 (45)	<b>Plenary Talk 1</b> "Aiming for an Environmental-Oriented CE Platform" Yoshiaki Kushiki (Panasonic)		<b>11/3</b> <b>16:30~17:30 Internet Lounge</b> <small>FUKUOKA IST</small>				
09:40-10:25 (45)	<b>Plenary Talk 2</b> "Foundry-Fabless Collaboration for Semiconductor SoC Industry in Korea" Young Hwan Oh (Dongbu HiTek Semiconductor)						
10:25-10:55 (30)	Break						
10:55-12:35 (100)	<b>Industry Program 1</b> Processors and Multimedia Circuits Stefan Rusu (Intel) Shaojun Wei (Phoenix Microelectronics)	<b>Industry Program 2</b> High-Speed Signaling and Interfaces Changhyun Kim (Samsung Electronic) Co-chair: Kji Kai (Panasonic)	<b>11/4</b> <b>12:35~ 13:40 Each Session Room</b> <small>Panel Exhibition by Fukuoka IST</small>				
12:35-13:40 (65)	Lunch						
13:40-15:45 (125)	<b>Session 1</b> Multi-stage A/D Converters Yong Moon (Soongsil Univ.) Koichi Ono (Sony)	<b>Session 2</b> Power and Delay Reduction Techniques for Digital Circuits Xiaoyang Zeng (Fudan Univ.) Hideyuki Kabuo (Panasonic)	<b>Session 3</b> Short-Range Interface Systems Yasumoto Tomita (Fujitsu Laboratories) Hiroyuki Mizuno (Hitachi)	<b>Session 4</b> mm-Wave CMOS Circuits Shouhei Kousai (Toshiba) Howard Luong (HKUST)	<small>Organizing Committee Office</small> <small>Internet Lounge</small>		
15:45-15:50 (5)	Break						
15:50-17:30 (100)	<b>Panel Discussion 1</b> SiP2.0: What, When, and How? Masayuki Mizuno (NEC Electronics)	<b>Panel Discussion 2</b> Digitally Assisted Analog and RF Circuits: Potentials and Issues Asad A. Abidi (UCLA)	<b>11/3</b> <b>16:00~17:30 Internet Lounge</b> <small>SDC Exhibition</small>				
17:30-18:00 (30)	Break						
18:00-18:45 (45)	<b>Special Lecture</b> How to Write a Good JSSC Paper Bram Nauta (Univ. of Twente)		<b>11/4</b> <b>17:30~18:00 Navis C</b>				
18:45-19:00 (15)	Break						
19:00-21:00 (120)					Banquet		

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8:30~ 9:30 Each Session Room

Nov. 5	Navis-C (PC1)	Navis-B (PC2)	Navis-A (PC3)	Argos-F (PC4)	Argos A&B	Nire	Kusu		
08:30-09:15 (45)	<b>Plenary Talk 3</b> "4G Wireless Technology: When will it Happen? What does it Offer?" Bill Krenik (Texas Instruments)								
09:15-09:50 (35)	Break								
09:50-12:20 (150)	<b>Session 5</b> <b>Advanced Power Management</b> Yasuhiro Sugimoto (Chuo Univ.) Bill Liu (Analog Devices, Shanghai)	<b>Session 6</b> <b>Multimedia Signal Processing</b> Takeshi Ikeraga (Waseda Univ.) Oh-Kyong Kwon (Hanyang Univ.)	<b>Session 7</b> <b>Wireline Communication</b> Woo Geun Rhee (Tsinghua Univ.) Hiroyuki Okada (NEC Electronics)	<b>Session 8</b> <b>Memory</b> Hiroyuki Yamauchi (Fukuoka Inst. of Tech.) Jae-Yoon Sim (POSTECH)		Organizing Committee Office	Internet Lounge		
12:20-13:20 (60)	Lunch								
13:20-15:25 (125)	<b>Session 9</b> <b>Analog Circuit Technique</b> Sung Min Park (Ewha Womans Univ.) Tsung-Hsien Lin (National Taiwan Univ.)	<b>Session 10</b> <b>Communication Signal Processing</b> Ramchan Woo (LG Electronics) Tai-cheng Lee (National Taiwan University)	<b>Session 11</b> <b>Electronics for Health</b> Ali Keshavarzi (TSMC) Reiji Hattori (Kyushu Univ.)	<b>Session 12</b> <b>RF Transceiver Circuits</b> Kang-Yoon Lee (Konkuk Univ.) Ryangsu Kim (Panasonic)					
15:25-15:45 (20)	Break								
15:45-18:15 (150)	<b>Session 13</b> <b>Sigma-Delta and Flash Data Converters</b> Hung-Sung Li (MediaTek) Sanroku Tsukamoto (Fujitsu Laboratories)	<b>Session 14</b> <b>Measurement &amp; Characterization of Digital Circuits</b> Makoto Ikeda (Univ. of Tokyo) Vasantha Erraguntla (Intel Technology India)	<b>Session 15</b> <b>Clock Generation Circuits</b> Srikanth Gond (K-Micro America) Koichiro Mashiko (Renesas Technology)	<b>Session 16</b> <b>RF Amplifiers and VCOs</b> Minoru Fujishima (Univ. of Tokyo) Hyunchol Shin (Kwangwoon Univ.)					

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17:30~ 19:00 Internet Lounge

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12:20~ 13:20 Each Session Room