

A-SSCC2008 Program at a Glance (Preliminary)

Nov. 3	Navis-C	Navis-B	Navis-A	Argos-F	Argos A&B	Nire	Kusu		
09:00-10:20 (80)	Tutorial 1 Design of Femto-joule Energy Efficient ADCs in CMOS Geert Van der Plas (IMEC)	Tutorial 2 Advanced SIP design Joungho Kim (KAIST)							
10:20-10:40 (20)	Break								
10:40-12:00 (80)	Tutorial 1 Design of Femto-joule Energy Efficient ADCs in CMOS Geert Van der Plas (IMEC)	Tutorial 2 Advanced SIP design Joungho Kim (KAIST)		Panel Exhibition by Fukuoka Industry, Science & Technology Foundation (Fukuoka IST) http://www2.lab-ist.jp/english/		Organizing Committee Office	Internet Lounge		
12:00-13:00 (60)	Lunch								
13:00-14:20 (80)	Tutorial 3 Advanced Clock Distribution System Simon Tam (Intel)	Tutorial 4 Economic and Design Choices for Nano-scale Electronic Systems Siva Narendra (Tyfone)							
14:20-14:40 (20)	Break								
14:40-16:00 (80)	Tutorial 3 Advanced Clock Distribution System Simon Tam (Intel)	Tutorial 4 Economic and Design Choices for Nano-scale Electronic Systems Siva Narendra (Tyfone)							
16:00-17:30 (90)				Student Design Contest (SDC)					
17:30-19:00 (90)	Welcome Reception								

Nov. 4	Navis-C	Navis-B	Navis-A	Argos-F	Argos A&B	Nire	Kusu	
08:30-08:40 (10)	Opening Ceremony							
08:40-08:50 (10)	Welcome Speech of Fukuoka Prefectural Governor							
08:50-09:35 (45)	Plenary Talk 1 "Aiming for an Environmental-Oriented CE Platform" Yoshiaki Kushiki (Panasonic)			Panel Exhibition by Fukuoka IST		Organizing Committee Office	Internet Lounge	
09:40-10:25 (45)	Plenary Talk 2 " Foundry-Fabless Collaboration for Semiconductor SoC Industry in Korea" Young Hwan Oh (Dongbu HiTek Semiconductor)							
10:25-10:55 (30)	Break			SDC Exhibition				
10:55-12:35 (100)	Industry Program 1 Processors and Multimedia Circuits Stefan Rusu (Intel) Shaojun Wei (Phoenix Microelectronics)	Industry Program 2 High-Speed Signaling and Interfaces Changhyun Kim (Samsung Electronic) Co-chair: Koji Kai (Panasonic)		Panel Exhibition by Fukuoka IST				
12:35-13:40 (65)	Lunch							
13:40-15:45 (125)	Session 1 Multi-stage A/D Converters Yong Moon (Soongsil Univ.) Koichi Ono (Sony)	Session 2 Power and Delay Reduction Techniques for Digital Circuits Xiaoyang Zeng (Fudan Univ.) Hideyuki Kabuo (Panasonic)	Session 3 Short-Range Interface Systems Yasumoto Tomita (Fujitsu Laboratories) Hiroyuki Mizuno (Hitachi)	Session 4 mm-Wave CMOS Circuits Shouhei Kousai (Toshiba) Howard Luong (HKUST)				
15:45-15:50 (5)	Break							
15:50-17:30 (100)	Panel Discussion 1 SIP2.0: What, When, and How? Masayuki Mizuno (NEC Electronics)	Panel Discussion 2 Digitally Assisted Analog and RF Circuits: Potentials and Issues Asad A. Abidi (UCLA)		Panel Exhibition by Fukuoka IST				
17:30-18:00 (30)	Break			SDC Exhibition				
18:00-18:45 (45)	Special Lecture How to Write a Good JSSC Paper Bram Nauta (Univ. of Twente)			Panel Exhibition by Fukuoka IST				
18:45-19:00 (15)	Break							
19:00-21:00 (120)					Banquet			

Nov. 5	Navis-C	Navis-B	Navis-A	Argos-F	Argos A&B	Nire	Kusu	
08:30-09:15 (45)	Plenary Talk 3 "4G Wireless Technology: When will it Happen? What does it Offer?" Bill Krenik (Texas Instruments)					Organizing Committee Office	Internet Lounge	
09:15-09:50 (35)	Break							
09:50-12:20 (150)	Session 5 Advanced Power Management Yasuhiro Sugimoto (Chuo Univ.) Bill Liu (Analog Devices, Shanghai)	Session 6 Multimedia Signal Processing Takeshi Ikenaga (Waseda Univ.) Oh-Kyong Kwon (Hanyang Univ.)	Session 7 Wireline Communication Woo Geun Rhee (Tsinghua Univ.) Hiroyuki Okada (NEC Electronics)	Session 8 Memory Hiroyuki Yamauchi (Fukuoka Inst. of Tech.) Jae-Yoon Sim (POSTECH)				
12:20-13:20 (60)	Lunch							
13:20-15:25 (125)	Session 9 Analog Circuit Technique Sung Min Park (Ewha Womans Univ.) Tsung-Hsien Lin (National Taiwan Univ.)	Session 10 Communication Signal Processing Ramchan Woo (LG Electronics) Tai-cheng Lee (National Taiwan University)	Session 11 Electronics for Health Ali Keshavarzi (TSMC) Reiji Hattori (Kyushu Univ.)	Session 12 RF Transceiver Circuits Kang-Yoon Lee (Konkuk Univ.) Ryansu Kim (Panasonic)				
15:25-15:45 (20)	Break							
15:45-18:15 (150)	Session 13 Sigma-Delta and Flash Data Converters Hung-Sung Li (MediaTek) Sanroku Tsukamoto (Fujitsu Laboratories)	Session 14 Measurement & Characterization of Digital Circuits Makoto Ikeda (Univ. of Tokyo) Vasantha Erraguntla (Intel Technology India)	Session 15 Clock Generation Circuits Srikanth Gondi (K-Micro America) Koichiro Mashiko (Renesas Technology)	Session 16 RF Amplifiers and VCOs Minoru Fujishima (Univ. of Tokyo) Hunchol Shin (Kwangwoon Univ.)				