

IEEE Asian Solid-State Circuits Conference 2011
IEEE A-SSCC 2011
Ramada Plaza Hotel, Jeju, Korea /November 14-16, 2011

Room A (Ramada Ballroom 1)

Session Title	[Industry 1] System-on-Chip Innovations
Session Chairs	Stefan Rusu (Intel Corporation, United States) Koji Kai (Panasonic Corp., Japan)
Date	November 15, 2011 (Tuesday)
Time	10:50~12:20

- Industry 1-1 10:50 - 11:20 An 18ms-Latency Wireless High Quality Codec SoC for Full HD Streaming**
Pilsoon Choi, Yongseok Yi, Kilsik Ha, Yun-Gu Lee, Chil-Youl Yang, Seyoung Shin, Byung-Ho Ahn, Sung-Chul Park, Hyun-Tae Gil, Scott Lee, Joongsuk Park, and Jaemoon Jo
- Industry 1-2 11:20 - 11:50 The Second Generation Intel® Core™: a Highly Integrated High Performance Multi IA-Core and Processor Graphics Chip**
Marcelo Yuffe, Omer Vikinski, Ziv Shmueli, Ernest Knoll, and Tsvika Kurts
- Industry 1-3 11:50 - 12:20 A Dynamic SIMD/MIMD Mode Switching Processor for Embedded Real-time Image Recognition Systems**
shohei nomoto, shorin kyo, and shinichiro okazaki