

**Room B (Ramada Ballroom 2)**

<b>Session Title</b>	<b>[Industry 2] Power and Signaling Building Blocks</b>
<b>Session Chairs</b>	Ron Ho (Oracle, Taiwan) Toru Shimizu (Renesas Electronics Corp., Japan)
<b>Date</b>	November 15, 2011 (Tuesday)
<b>Time</b>	10:50~12:20

- Industry 2-1    10:50 - 11:20    0.8-V Start-up 92% Efficiency On-Chip Boost DC-DC Converters for Battery Operation Micro-Computers**  
Yasunobu Nakase, Shinichi Hirose, Toru Goda, Kehui Hu, Hiroshi Onoda, Yasuhiro Ido, Hiroyuki Kono, Wei Kong, Wei Zhang, Tsukasa Oishi, Shintaro Mori, and Toru Shimizu
- Industry 2-2    11:20 - 11:50    On Overcoming the Limitations of Single-Ended Signaling for Graphics Memory Interfaces**  
Amir Amirkhany, Wendem Beyene, Chris Madden, Aliazam Abbasfar, Dave Secker, Dan Oh, Mohammad Hekmat, Ralf Schmitt, and Chuck Yuan
- Industry 2-3    11:50 - 12:20    Digitally-assisted analog circuits for a 10 Gbps, 395 fJ/b optical receiver in 40 nm CMOS**  
Philip Amberg, Frankie Liu, Michael Dayringer, Jon Lexau, Dinesh Patil, Jon Gainsley, Hesam Fathi Moghadam, Elad Alon, Xuezhe Zheng, John Cunningham, Ashok Krishnamoorthy, and Ron Ho