

Room A (Ramada Ballroom 1)

Session Title	[Session 1] Power Management
Session Chairs	Yasuhiro Sugimoto (Chuo University, Japan) Jeongjin Roh (Hanyang University, Korea)
Date	November 15, 2011 (Tuesday)
Time	13:30~15:35

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|------------|----------------------|--|
| 1-1 | 13:30 - 13:55 | A 80-mV Input, 56 Times Faster Startup Dual-Mode Boost Converter with Charge-Pumped Pluse Generator for Energy Harvesting
Po-Hung Chen, Koichi Ishida, Xin Zhang, Yasuyuki Okuma, Yoshikatsu Ryu, Makoto Takamiya, and Takayasu Sakurai |
| 1-2 | 13:55 - 14:20 | A 1.39-V Input Fast-Transient-Response Digital LDO Composed of Low-Voltage MOS Transistors in 40-nm CMOS Process
Masafumi Onouchi, Kazuo Otsuga, Yasuto Igarashi, Toyohito Ikeya, Sadayuki Morita, Koichiro Ishibashi, and Kazumasa Yanagisawa |
| 1-3 | 14:20 - 14:45 | A High Stability DC-DC Boost Converter with Ripple Current Control and Capacitor-Free LDOs for AMOLED Display
Se-Won Wang, Young-Jin Woo, Sung-Ho Bae, Tae-Hwang Kong, Gyu-ha Cho, and Gyu-Hyeong Cho |
| 1-4 | 14:45 - 15:10 | An Asynchronous Digitally-Controlled Switching Converter with Adaptive Resolution and Dynamic Power Saving to Achieve Higher Than 93.5% Efficiency between 5mA and 250mA Load
Po-Hsiang Lan, Tsung-Ju Yang, and Po-Chiun Huang |
| 1-5 | 15:10 - 15:22 | A MOS Current-Mode Boost DC-DC Converter with the Duty-Ratio-Independent Frequency Characteristics
Yuya Hirano and Yasuhiro Sugimoto |
| 1-6 | 15:22 - 15:34 | A Fast-Transient Quasi-V2 Switching Buck Regulator Using |

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AOT Control

Huang Chun-Sheng, Wang Jia-Hui, Wang Chen-Yu, and Tsai
Chien-Hung