A-SSCC 2012 Program at a Glance

Nov.12 (Mon.)

Time	Room 401 4F		Room 402 4F	Reception Hall 3F	Lobby 4F
9:00 - 9:30 (30')					
9:30 - 10:50 (80')	Tutorial Ⅰ "SoC Power Reduction and Management Techniques" Stefan Rusu (Intel)		Tutorial Ⅱ "Designing CMOS Wireless LAN System-on-a-Chip" Srenik Mehta (Qualcomm Atheros)		On-Site Registration (9:00-17:30)
10:50 - 11:05 (15')	Break				
11:05 - 12:25 (80')	Tutorial Ⅰ "SoC Power Reduction and Management Techniques" Stefan Rusu (Intel)		Tutorial Ⅱ "Designing CMOS Wireless LAN System-on-a-Chip" Srenik Mehta (Qualcomm Atheros)		
12:25 - 13:25 (60')	Lunch				
13:25 - 14:45 (80')	Tutorial Ⅲ "High Performance Non-Volatile Memory Design in Nano-Scale Era" Sungdae Choi (SK hynix)		Tutorial Ⅳ "Smart Sensor Design in Standard CMOS" Prof. Kofi Makinwa (Delft Univ. of Technology)		
14:45 - 15:00 (15')	Break				
15:00 - 16:20 (80')	Tutorial Ⅲ "High Performance SRAM Design in Deep Nanometer Technologies" Jente B Kuang (IBM)		Tutorial Ⅳ "Smart Sensor Design in Standard CMOS" Prof. Kofi Makinwa (Delft Univ. of Technology)		
16:20 - 17:30 (70')				SDC Exibition (16:00-18:30)	
17:30 - 19:30 (120')	Welcome Reception (Reception Hall)				
19:00 - 19:30 (30')	TPC Meeting (Room 401+402)				

Nov. 13 (Tue.)

Time	International Conference Room Room 301 3F	Room 406	Room 401,Room 402 4F	Room 403	Reception Hall 3F		Robby
8:30 - 9:00 (30')							
9:00 - 9:10 (10')	Opening Ceremony						
9:10 - 9:20 (10')	Welcome Speech						
9:20 - 10:05 (45')	Plenary Talk 1 "Expectations for the Semiconductor Technologies in EVs and HVs" Mr. Shoichi Sasaki (Professor, Keio Univ., Japan)						On-Site Registration (8:30-17:30)
10:05 - 10:50 (45')	Plenary Talk 2 "Semiconductor Memory Scaling and Beyond" Dr. Sungjoo Hong (SVP, SK hynix Inc., Korea)						
10:50 - 11:05 (15')	Break				SDC Exhibition	Supporter's Exhibition	
11:05 - 12:45 (100')	Industry Program 1 Leading Edge SoCs and Memory		Industry Program 2 Energy Efficient Circuits for Emerging Applications				
12:45- 13:45 (60')	Lunch						
13:45 - 15:50 (125')	Session 1 Analog Interfaces and Amplifiers	Session 2 Advanced Memory	Session 3 TX RX Architecture and Building Blocks	Session 4 Energy Efficient Circuits and Techniques			
15:50 - 16:05 (15')	Break				SDC Exhibition		
16:05 - 17:45 (100')	Panel Discussion 1 Disruptive design for emerging technology after 3D Devices/FinFET and beyond; How can make it up?		Panel Discussion 2 Challenge for Zero Stand-by Power Management - Road-map to the "Normally-Off Computing" -				
17:45 - 18:00 (15')							
18:00 - 18:30 (30')	Shuttle to Cruiser (Meet at 17:55 at Lobby, 2F)						
19:00-21:20 (140')	Banquet (Kobe Cruise)						

Nov.14 (Wed.)

Time	International Conference Room Room 301 3F	Room 401	Room 402 4F	Room 403	Reception Hall 3F	Robby
8:30 - 9:00 (30')						
9:00 - 9:45 (45')	Plenary Talk 3 "Integrated Circuits and Systems toward Smart Ubiquitous Patient-Centered Medical Environment" Dr. Ming-Fong Chen (Superintendent, NTU Hospital, Taiwan)					On-Site Registration (8:30-12:00)
9:45 - 10:30 (45')	Plenary Talk 4 "Technology Challenges and Opportunities for Ubiquitous Computing" Dr. Shekhar Borkar (Intel Fellow, Intel Corp., U.S.A.)					
10:30 - 10:45 (15')	Break					
10:45 - 12:50 (125')	Session 5 High speed Transceivers and Building Block	Session 6 Nyquist-Rate ADCs	Session 7 Emerging Biomedical Circuits and Systems	Session 8 Low-Power Digital Communication & Multimedia SoCs	Supporter's Exhibition	
12:50 - 13:50 (60')	Lunch					
13:50 - 15:55 (125')	Session 9 Power Management ICs	Session 10 Oversampling ADCs	Session 11 Millimeter-Wave Circuits and Systems	Session 12 Clock Generation and Timing Circuits		
15:55 - 16:10 (15')	Break					
16:10 - 18:15 (125')	Session 13 SSD Memory and High Frequency Analog	Session 14 Ultra Low-Power Circuits for Emerging Communication System	Session 15 VCO & PLL	Session 16 Low-Power SoCs & Circuits		